LI0201C121R-10

UNCONTROLLED

PHYSICAL DIMENSIONS:

- 60 mA

180 mA

-300 mA

10

AGILENT E4991A RF Impedance/Material Analyzer AGILENT 16196C Test Fixture. Test Ref. 6274

160

120

IMPEDANCE

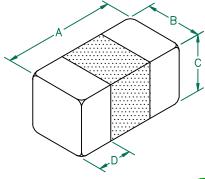
A 0.60 [.024] ± 0.03 [.001]

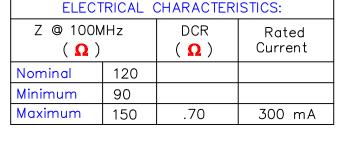
B 0.30 [.012] ± 0.03 [.001]

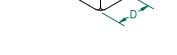
± 0.03 [.001] C 0.30 [.012]

D 0.15 [.006] ± 0.05 [.002]

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



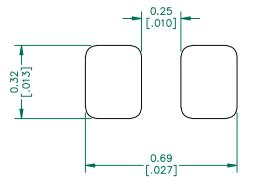


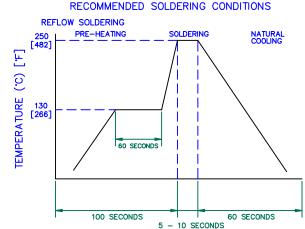


NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 15,000 PCS/REEL, PAPER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.

LAND PATTERNS FOR REFLOW SOLDERING





			Z , R	, AND X vs.	FREQUENC	Y
	200					
	180					
	160			//		
(=	140					
)	120					
IMPEDANCE (0	100			7 / R		
AN	80 -					
PEI	60 -					
≥	40 -					
	20			1		
	0 -					
		1	10	100	1000	10000
				FREQUENCY	′ (MHz)	
				F	1	

FREQUENCY (MHz)



	5 - 10 SECONDS									
		DIMENSIONS ARE IN mm [INCHI	ES].	This print is the property of Laird						
					written consent of Laird Tech. All rights to design or invention are reserved.	l				
Į.					PROJECT/PART NUMBER: REV PART TYPE	: DRAWN BY:				
L					LI0201C121R-10 C CO-F	TRE JRK				
L	С	CORRECT DCR VALUE ADD TEST REF.	09/04/08							
	В	ADD CURVES	08/20/08	JRK	DATE: 07/07/08 SCALE: NTS	SHEET:				
ı	A	ORIGINAL DRAFT	07/07/08	JRK		2 of 2				
Ī	REV	DESCRIPTION	DATE	INT		2 0, 2				